



CofC

Attorney's Docket No. MTI-31269

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 6,794,704 B2 10,050,390
Issued : September 21, 2004
Patentee : Donald L. Yates et al.
Title : Method for Enhancing Electrode Surface Area in DRAM Cell Capacitors

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being:

Mailing

- deposited with the U.S. Postal Service in an envelope addressed to the Commissioner for Patents, Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450.

37 CFR 1.8(a)

37 CFR 1.10

- with sufficient postage as first class mail ☐ As "Express Mail Post Office to Addressee" Mailing Label No.

Transmission

- ☐ transmitted by facsimile to Fax No. _____ addressed to _____ at the U.S. Patent and Trademark Office.

Date: September 28, 2004

Jim R. Hawk

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTENTION: Certificate of Correction Branch

Certificate
OCT 06 2004
of Correction

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR PTO MISTAKE (37 C.F.R. SECTION 1.322(A))

Sir:

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States Patent.

Attached is Form PTO-1050, with the text of the Certificate in the suggested form suitable for printing.

The column and line number where the errors occur in the issued patent are as follows:

Column 12, line 5: Insert "layer" after --texturizing--.

REMARKS

The errors sought to be corrected in the specification are Patent Office printing errors.

OCT 08 2004

Supporting documentation includes a copy of an Amendment Under 37 CFR §1.312 filed on July 2, 2004, with the relevant replacement pages for the claims, showing that no amendments were made to Claim 160 (which corresponds to issued Claim 32).

The requested corrections are to correct a printing error in the claims to conform with the specification and claims as allowed by the Examiner during prosecution. Issuance of a Certificate of Correction would not change either the scope or the meaning of the specification, and re-examination is not required.

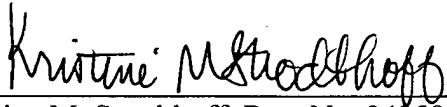
As the errors listed are due to the Patent Office's printing mistakes, no fee is necessary in connection with this Certificate.

The Examiner is requested to contact the undersigned Attorney for Applicant should any questions arise with respect to this Request.

Please send the Certificate of Correction to:

Kristine M. Strodthoff
Whyte Hirschboeck Dudek S.C.
555 East Wells Street, Suite 1900
Milwaukee, WI 53202-3819

Dated: September 28, 2004



Kristine M. Strodthoff, Reg. No. 34259
Attorney of Record

P.O. ADDRESS:

WHYTE HIRSCHBOECK DUDEK S.C.
555 East Wells Street, Suite 1900
Milwaukee, Wisconsin 53202-3819
(414) 273-2100
Customer No. 31870

OCT 08 2004

**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : 6,794,704 **B2**
DATED : September 21, 2004
INVENTOR(S) : Donald L. Yates

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 12, line 5: Insert "layer" after --texturizing--.

MAILING ADDRESS OF SENDER:

Whyte Hirschboeck Dudek S.C.
555 East Wells Street, Suite 1900
Milwaukee, Wisconsin 53202-3819

PATENT NO. 6,794,704 **B2**

No. of additional copies



This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

OCT 08 2004



MAIL STOP ISSUE FEE

Notice of Allowance mailed: May 03, 2004

PATENT

Attorney Docket No. MTI-31269

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Yates, et al.
Serial No. : 10/050,390
Filing Date : January 16, 2002
Examiner : NGUYEN, Cuong Quang
Group Art Unit: 2811
For : Method for Enhancing Electrode Surface Area in DRAM Cell Capacitors
Confirmation No.: 6193

CERTIFICATION UNDER 37 CFR 1.8(a) and 1.10

I hereby certify that, on the date shown below, this correspondence is being transmitted to Fax No. 703-872-9306 addressed to Examiner NGUYEN at the US Patent and Trademark Office.

Date:

7-2-4

Patricia Paige Peckun

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT UNDER 37 CFR § 1.312

Sir:

Applicant requests amendment to the pending claims in the above-identified patent application.

Amendments to the Claims are reflected in the listing of the claims, which begins on *page 3* of this paper.

Remarks begin on *page 10* of this paper.

irradiation and ozonolysis of a polymeric material comprising a hydrocarbon block and a silicon-containing block:

159. (canceled)

160. (previously presented) A capacitor, comprising:

a lower capacitor plate comprising a conductive layer overlying a texturizing layer; the texturizing layer comprising an ordered array of nanostructures of substantially uniform size, and the texturizing layer comprising a polymeric material;

a dielectric layer overlying the lower capacitor plate; and

an upper capacitor plate overlying the dielectric layer.

161. (canceled)

162. (previously presented) The capacitor of Claim 160, wherein the polymeric material comprises a hydrocarbon block and a silicon-containing block.

163. (previously presented) The capacitor of Claim 162, wherein the polymeric material comprises polyisoprene and poly(pentamethyldisilylstyrene).

164-167. (canceled)

168. (previously presented) The capacitor of Claim 160, wherein the texturizing layer comprises a plurality of two-dimensional structures.

169. (previously presented) A capacitor, comprising:

a lower capacitor plate comprising a conductive layer overlying a texturizing layer; the texturizing layer comprising a periodic network of surface structures having a substantially uniform height, and the texturizing layer comprising a polymeric material;